



MAR 27 2003

S/N 09/945500

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes  
Serial No.: 09/945500  
Filed: August 30, 2001  
Title: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH  
LOW TUNNEL BARRIER INTERPOLY INSULATORS

Examiner: Ly-D. Pham

Group Art Unit: 2818

Docket: 1303.029US1

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. § 1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. § 1.17(p). Please charge any additional fees or credit any overpayment to Account No. 19-0743.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LEONARD FORBES

By his Representatives,

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Date 24 March 2003

By 

Timothy B. Clise

Reg. No. 40,957

**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this day of March, 2003

Name

Amy Moriarty

Signature

